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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/650,852	08/29/2003	Katsuhiko Hieda	04329.3128	7406
7590 01/25/2005		5	EXAMINER	
Finnegan, Henderson, Farabow,			WOJCIECHOWICZ, EDWARD JOSEPH	
Garrett & Dunner, L.L.P.				
1300 I Street, N.W.			ART UNIT	PAPER NUMBER
Washington, DC 20005-3315			2815	

DATE MAILED: 01/25/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	_			
Office Action Summary		10/650,852	HIEDA, KATSUHIKO	HIEDA, KATSUHIKO			
		Examiner	Art Unit	_			
		Edward Wojciechowicz	2815				
Period fo	The MAILING DATE of this communication Reply	on appears on the cover sheet w	ith the correspondence address				
THE - External after - If the control of the contro	ORTENED STATUTORY PERIOD FOR F MAILING DATE OF THIS COMMUNICAT nasions of time may be available under the provisions of 37 C SIX (6) MONTHS from the mailing date of this communicatic period for reply specified above is less than thirty (30) days period for reply is specified above, the maximum statutory are to reply within the set or extended period for reply will, by reply received by the Office later than three months after the ed patent term adjustment. See 37 CFR 1.704(b).	ION. FR 1.136(a). In no event, however, may a solon. In a reply within the statutory minimum of thir period will apply and will expire SIX (6) MON a statute, cause the application to become Al	eply be timely filed ty (30) days will be considered timely. ITHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).				
Status		·					
1) 🗌	Responsive to communication(s) filed on	29 October 2004.					
2a) <u></u> □	<u> </u>						
3) 🗌	3) Since this application is in condition for allowance except for formal matters, prosecution as to the ments is						
	closed in accordance with the practice ur	nder <i>Ex par</i> te Quayle, 1935 C.[). 11, 453 O.G. 213.				
Disposit	ion of Claims						
4) 🖾	Claim(s) 1-14 is/are pending in the applic	ation.					
	4a) Of the above claim(s) <u>7-12</u> is/are withdrawn from consideration.						
5)	Claim(s) is/are allowed.						
6)⊠	☑ Claim(s) <u>1-6,13 and 14</u> is/are rejected.						
7)							
8)∐	Claim(s) are subject to restriction	and/or election requirement.					
Applicati	ion Papers						
9)[The specification is objected to by the Exa	aminer.					
10)□	The drawing(s) filed on is/are: a)	accepted or b) dbjected to	by the Examiner.				
	Applicant may not request that any objection	to the drawing(s) be held in abeya	nce. See 37 CFR 1.85(a).				
_	Replacement drawing sheet(s) including the o	•					
11)	The oath or declaration is objected to by t	he Examiner. Note the attache	d Office Action or form PTO-152.				
Priority (ınder 35 U.S.C. § 119						
12)	Acknowledgment is made of a claim for fo	oreign priority under 35 U.S.C.	§ 119(a)-(d) or (f).				
•	☐ All b)☐ Some * c)☐ None of:		, , , , ,				
	1. Certified copies of the priority docu	ments have been received.					
	2. Certified copies of the priority docu	ments have been received in A	pplication No				
•	3. Copies of the certified copies of the	•	received in this National Stage				
	application from the International E						
* 5	See the attached detailed Office action for	a list of the certified copies not	received.				
Attachmen	t(s)						
	e of References Cited (PTO-892)		Summary (PTO-413)				
	e of Draftsperson's Patent Drawing Review (PTO-94 mation Disclosure Statement(s)	· — —	s)/Mail Date nformal Patent Application (PTO-152)				
	mation Disclosure Statement(s) (PTO-1449 or PTO/: ir No(s)/Mail Date <u>8-29-03</u> .	6) Other:	· · · · · · · · · · · · · · · · · · ·				
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DETAILED ACTION

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Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 13 and 14 are rejected under 35 U.S.C. 102(b) as being anticipated by Kim et al. The reference to Kim shows all of the structural elements of claim 1. For example, as shown in Fig. 4B, the Kim device includes a bit line (32d) extending in a first direction, with a plurality of transistors connected to the bit line, a plurality of first electrodes (38) arranged in the first direction and electrically connected to the transistors, a dielectric film (39) covering the upper and side surfaces of the first electrodes, a second electrode (42), shown in Fig. 7, covering the dielectric film, where the width of the first electrode is smaller than a distance between adjacent first electrodes.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 2-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kim in view of Matsuoka. While Kim teaches the basic inventive structure of the invention, he does not explicitly describe the structure where the angle between the longitudinal direction of the bit line and the first electrodes is an acute angle. Matsuoka shows such a configuration. See, for example, Fig. 20, and the discussion at col. 3, I. 40-60. In this configuration, a line parallel to the longitudinal direction of first electrodes (22) may make an acute angle with a line parallel to the first direction of the bit lines. Due to

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the matrix configuration of the memory cell of Matsuoka, an angle of 45 degrees would be a logical choice

to maximize device density, etc.

Similarly, with regard to the relative widths of the various lines, Matsuoka also teaches that the

width of the first electrode may be smaller than the width of the bit line. Compare, for example, the

relative widths in Fig. 21, where the width of first electrode (22) is appreciably smaller that that of the bit

line (20). See also, the discussion at col. 8, I. 9-23 of Matsuoka.

One skilled in the art would be motivated to combine these references in order to gain the

advantage of increasing the packing density of the memory device of Kim by utilizing the folded bit line

structure taught by Matsuoka.

Any inquiry concerning this communication or earlier communications from the examiner should

be directed to Edward Wojciechowicz whose telephone number is 571-272-1739. The examiner can

normally be reached on Monday through Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom

Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this

application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application

Information Retrieval (PAIR) system. Status information for published applications may be obtained from

either Private PAIR or Public PAIR. Status information for unpublished applications is available through

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you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC)

at 866-217-9197 (toll-free).

Edward Wojciechowicz Primary Examiner

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EW: ew